

GENERAL DESCRIPTION

AVX is the market leader in the development and manufacture of capacitor arrays. The smallest array option available from AVX the 0405 2-element device, has been an enormous success in the Telecommunications market. The array family of products also includes the 0612 4-element device as well as 0508 2-element and 4-element series, all of which have received widespread acceptance in the marketplace.

AVX capacitor arrays are available in X5R, X7R and NPO (COG) ceramic dielectrics to cover a broad range of capacitance values. Voltage ratings from 6.3 Volts up to 100 Volts are offered.

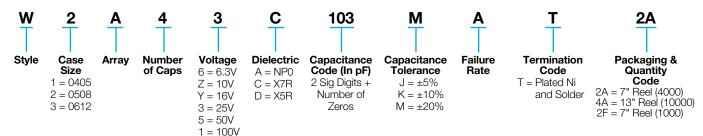
Key markets for capacitor arrays are Mobile and Cordless Phones, Digital Set Top Boxes, Computer Motherboards and Peripherals as well as Automotive applications, RF Modems, Networking Products etc.

AVAILABLE CAPACITANCE VALUES

Case	V 11	NP0	/C0G	X5R/	X7R
Size	Voltage	Min. Cap	Max. Cap.	Min. Cap.	Max. Cap.
	16v	100	471	221	104
0612	25v	100	471	221	473
4 element	50v	100	471	221	473
	100v	100	391	221	103
	10v				104
	16v	100	271	221	563
0508	25v	100	271	221	183
4 element	50v	100	271	221	183
	100v	100	221	221	472
	6.3v				105
	16v	100	471	221	104
0508	25v	100	471	221	333
2 element	50v	100	471	221	333
	100v	100	391	221	822
	10v				104
0405	16v	100	101	121	223
2 element	25v	100	101	121	682
	50v	100	101	121	682

= X5R (Above 103)

HOW TO ORDER





Capacitor Array (IPC)



BENEFITS OF USING CAPACITOR ARRAYS

AVX capacitor arrays offer designers the opportunity to lower placement costs, increase assembly line output through lower component count per board and to reduce real estate requirements.

Reduced Costs

Placement costs are greatly reduced by effectively placing one device instead of four or two. This results in increased throughput and translates into savings on machine time. Inventory levels are lowered and further savings are made on solder materials etc.

Space Saving

Space savings can be quite dramatic when compared to the use of discrete chip capacitors. As an example, the 0508 4-element array offers a space reduction of >40% vs. 4 x 0402 discrete capacitors and of >70% vs. 4 x 0603 discrete capacitors. (This calculation is dependent on the spacing of the discrete components.)

Increased Throughput

Assuming that there are 220 passive components placed in a mobile phone:

A reduction in the passive count to 200 (by replacing discrete components with arrays) results in an increase in throughput of approx. 9%.

A reduction of 40 placements increases throughput by 18%.

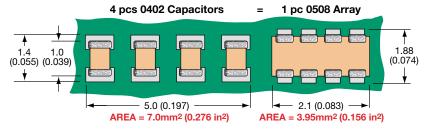
For high volume users of cap arrays using the very latest placement equipment capable of placing 10 components per second, the increase in throughput can be very significant and can have the overall effect of reducing the number of placement machines required to mount components:

If 120 million 2-element arrays or 40 million 4-element arrays were placed in a year the requirement for placement equipment would be reduced by one machine.

During a 20Hr operational day a machine places 720K components. Over a working year of 167 days the machine can place approximately 120 million. If 2-element arrays are mounted instead of discrete components, then the number of placements is reduced by a factor of two and in the scenario where 120 million 2-element arrays are placed there is a saving of one pick and place machine.

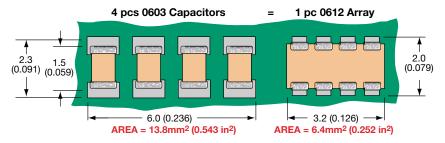
Smaller volume users can also benefit from replacing discrete components with arrays. The total number of placements is reduced thus creating spare capacity on placement machines. This in turn generates the opportunity to increase overall production output without further investment in new equipment.

W2A (0508) Capacitor Arrays



The 0508 4-element capacitor array gives a PCB space saving of over 40% vs four 0402 discretes and over 70% vs four 0603 discrete capacitors.

W3A (0612) Capacitor Arrays



The 0612 4-element capacitor array gives a PCB space saving of over 50% vs four 0603 discretes and over 70% vs four 0805 discrete capacitors.



Multi-Value Capacitor Array (IPC)



GENERAL DESCRIPTION

A recent addition to the array product range is the Multi-Value Capacitor Array. These devices combine two different capacitance values in standard 'Cap Array' packages and are available with a maximum ratio between the two capacitance values of 100:1. The multi-value array is currently available in the 0405 and 0508 2-element styles.

Whereas to date AVX capacitor arrays have been suited to applications where multiple capacitors of the same value are used, the multi-value array introduces a new flexibility to the range. The multi-value array can replace discrete capacitors of different values and can be used for broadband decoupling applications. The 0508 x 2 element multi-value array would be particularly recommended in this application. Another application is filtering the 900/1800 or 1900MHz noise in mobile phones. The 0405 2-element, low capacitance value NPO, (COG) device would be suited to this application, in view of the space saving requirements of mobile phone manufacturers.

ADVANTAGES OF THE MULTI-VALUE CAPACITOR ARRAYS

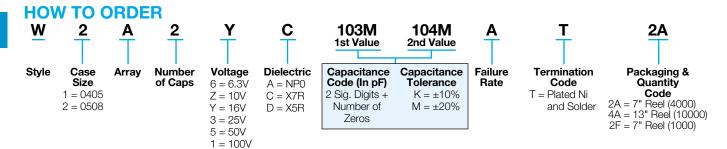
Enhanced Performance Due to Reduced Parasitic Inductance

When connected in parallel, not only do discrete capacitors of different values give the desired self-resonance, but an additional unwanted parallel resonance also results. This parallel resonance is induced between each capacitor's self-resonant frequencies and produces a peak in impedance response. For decoupling and bypassing applications this peak will result in a frequency band of reduced decoupling and in filtering applications reduced attenuation.

The multi-value capacitor array, combining capacitors in one unit, virtually eliminates the problematic parallel resonance, by minimizing parasitic inductance between the capacitors, thus enhancing the broadband decoupling/filtering performance of the part.

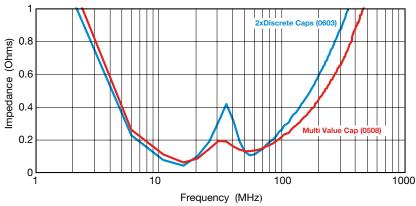
Reduced ESR

An advantage of connecting two capacitors in parallel is a significant reduction in ESR. However, as stated above, using discrete components brings with it the unwanted side effect of parallel resonance. The multi-value cap array is an excellent alternative as not only does it perform the same function as parallel capacitors but also it reduces the uncertainty of the frequency response.



IMPEDANCE vs FREQUENCY MULTI-VALUE ARRAY COMPARED TO DISCRETE CAPACITORS

10nF / 100nF Capacitor Impedance vs Frequency







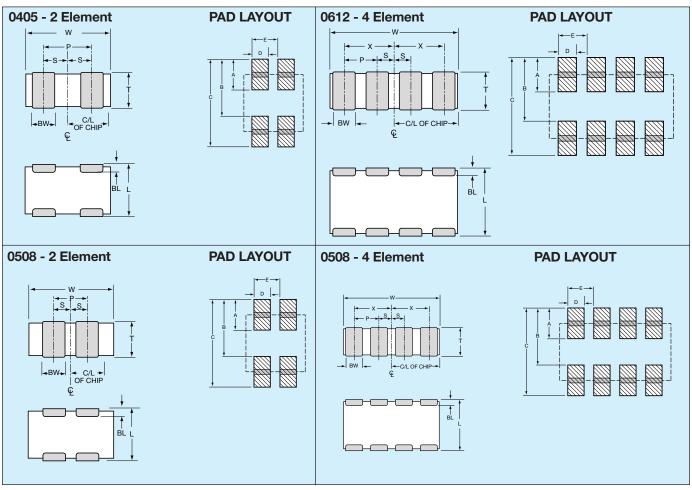
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# Elements Soldering	Re	2 low 0	nlv		Reflo	2	Vave			Refle	4 ow/W	lave		Refle	4 ow/Wa		# Elements Soldering	Re	2 eflow			Refl	2 ow/\	Jave			Refle	4 ow/W	ave	F	Reflov	1 ν////a\
Packaging		I Pape				Pap			Р		/Emb		d	Paper			Packaging		All Pa				II Pap						ssed	_	per/E	
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(in.)	_	37 ± 0.0		<u> </u>	2.10))	(0 ± 0		+		0.03 ± 0.00		(in.)	,		0.000)		`	0 ± 0)	(($0 \pm 0.$		_	3.20	
vviatri (in.)		4 ± 0.		(0.083	3 ± C	0.006	3)	(0.080	3 ± 0)		$26 \pm 0.$		(in.))54 ±	0.006)		(0.08	3 ± 0	.006)	(0	0.083	3 ± 0.			.126	± 0.0
Max. MM Thickness (in.)		0.66)			0.94).037					0.94).037)		(1.35)	Max. MM Thickness (in.)		(0.02				0.94 0.037					0.94).037)				.35 053)
WVDC	10	6 25	50	6.3		25		100	10	16	25	50	100	16 2	5 50	100	WVDC	10	16	25 50	6.3			50	100	10			50 10	16	<u> </u>	
CAP 1 pF 1.2																	CAP 100 pF 120															
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3.3 3.9																	330 390															
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33 39																	3300 3900															
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56 68																	5600 6800															
82																	8200															
100 120																	Cap 0.010 µF 0.012															
150																	0.015															
180 220																	0.018 0.022															
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330 390																	0.033 0.039															
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Cap 0.010 µF																	1.2 1.5															
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PART & PAD LAYOUT DIMENSIONS

millimeters (inches)



PART DIMENSIONS

0405 - 2 Element

L	W	Т	BW	BL	Р	S
1.00±0.15	1.37±0.15	0.66 MAX	0.36±0.10	0.20±0.10	0.64 REF	0.32±0.10
(0.039±0.006)	(0.054±0.006)	(0.026 MAX)	(0.014±0.004)	(0.008±0.004)	(0.025 REF)	(0.013±0.004)

0508 - 2 Element

L	W	T	BW	BL	Р	S
1.30±	 2.10±0.15	0.94 MAX	0.43±0.10	0.33±0.08	1.00 REF	0.50±0.10
(0.051±	(0.083±0.006)	(0.037 MAX)	(0.017±0.004)	(0.013±0.003)	(0.039 REF)	(0.020±0.004)

0508 - 4 Element

L	W	T	BW	BL	P	X	S
1.30±0.15	2.10±0.15	0.94 MAX	0.25±0.06	0.20±0.08	0.50 REF	0.75±0.10	0.25±0.10
(0.051±0.006)	(0.083±0.006)	(0.037 MAX)	(0.010±0.003)	(0.008±0.003)	(0.020 REF)	(0.030±0.004)	(0.010±0.004)

0612 - 4 Element

L	W	Т	BW	BL	Р	X	S
1.60±0.20 (0.063±0.008	3.20±0.20 (0.126±0.008)	1.35 MAX (0.053 MAX)	0.41±0.10 (0.016±0.004)	0.18 +0.25 0.18 -0.08 (0.007+0.010) -0.003	0.76 REF (0.030 REF)	1.14±0.10 (0.045±0.004)	0.38±0.10 (0.015±0.004)

PAD LAYOUT DIMENSIONS

0405 - 2 Element

	Α	В	С	D	Е
Г	0.46	0.74	1.20	0.30	0.64
L	(0.018)	(0.029)	(0.047)	(0.012)	(0.025)

0508 - 2 Element

Α	В	C	D	Е
0.68	1.32 (0.052)	2.00	0.46 (0.018)	1.00 (0.039)
(0.027)	(0.052)	(0.079)	(0.016)	(0.039)

0508 - 4 Element

Α	В	 С	D	Е
0.56 (0.022)	1.3	.88 074)	0.30 (0.012)	0.50 (0.020)

0612 - 4 Element

Α	В	С	D	Е
0.89	1.65	2.54	0.46	0.79
(0.035)	(0.065)	(0.100)	(0.018)	(0.031)





PERFORMANCE CHARACTERISTICS

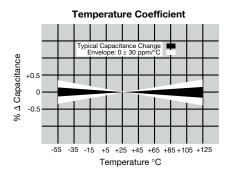
Standard Tolerance	NP0/C0G	X5R/X7R
	±10%	±20%
Dissipation Factor	0.1% Max.	50&100V = 2.5% Max.
_		25V = 3.0% Max.
		16V = 3.5% Max.
		10V = 5.0% Max.
Insulation Resistance	100,000 MΩ min, or	100,000 M Ω min, or
(@+25°C, RVDC)	1,000 MΩ per μF	1,000 MΩ per μF
	min., whichever is less	min.,whichever is less

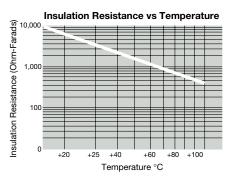
MECHANICAL SPECIFICATIONS

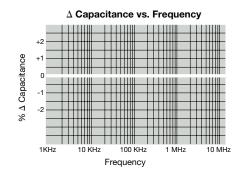
CTE (PPM / °C)	NP0/C0G: 10.5; X7R/X5R: 12.0		
Thermal Conductivity All Bodies 4 to 5 W/M ° K			
Terminations Plated Nickel and Solder			
Thickness	0.94mm Max. / 0.037" Max. (Based on Cap and Dielectric)		

TYPICAL CHARACTERISTIC CURVES

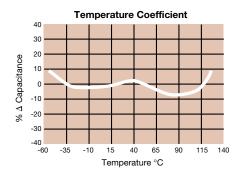
NP0 / COG DIELECTRIC

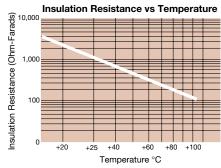


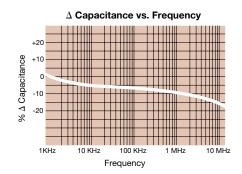




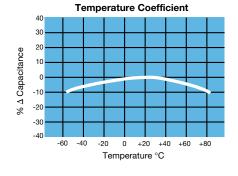
X7R DIELECTRIC

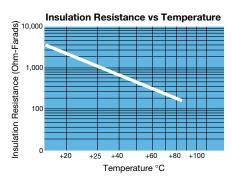


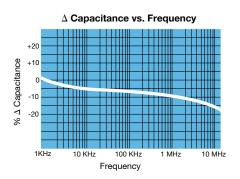




X5R DIELECTRIC











SMT Applications

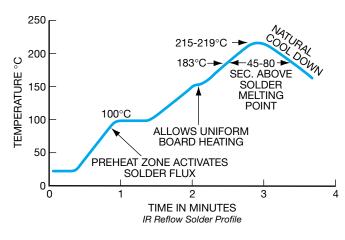
The following solderability profiles are suggested for the different soldering techniques.

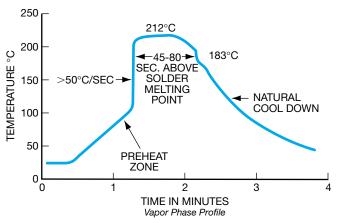
INFRARED REFLOW SOLDERING (IR)

Soldering IR has the highest yields due to controlled heating rates and solder liquidus times. Only the dwell time and peak temperature limitations of resin-molded components need to be considered. Typical recommended solder past wet laydown is approximately 8 mils.

VAPOR PHASE REFLOW SOLDERING

Vapor phase soldering has the second highest heat transfer rate so care must be used. Preheating the assembly and minimizing the dwell time above the solder liquidus temperature is required to help reduce defects.





ASSEMBLY NOTES

AVX recommends a minimum spacing of 0.020" between parts when placing them end to end on assembly boards. The purpose of this spacing is to facilitate post assembly board cleaning and inaccuracies in pick and placement, due to machine placement tolerances.

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